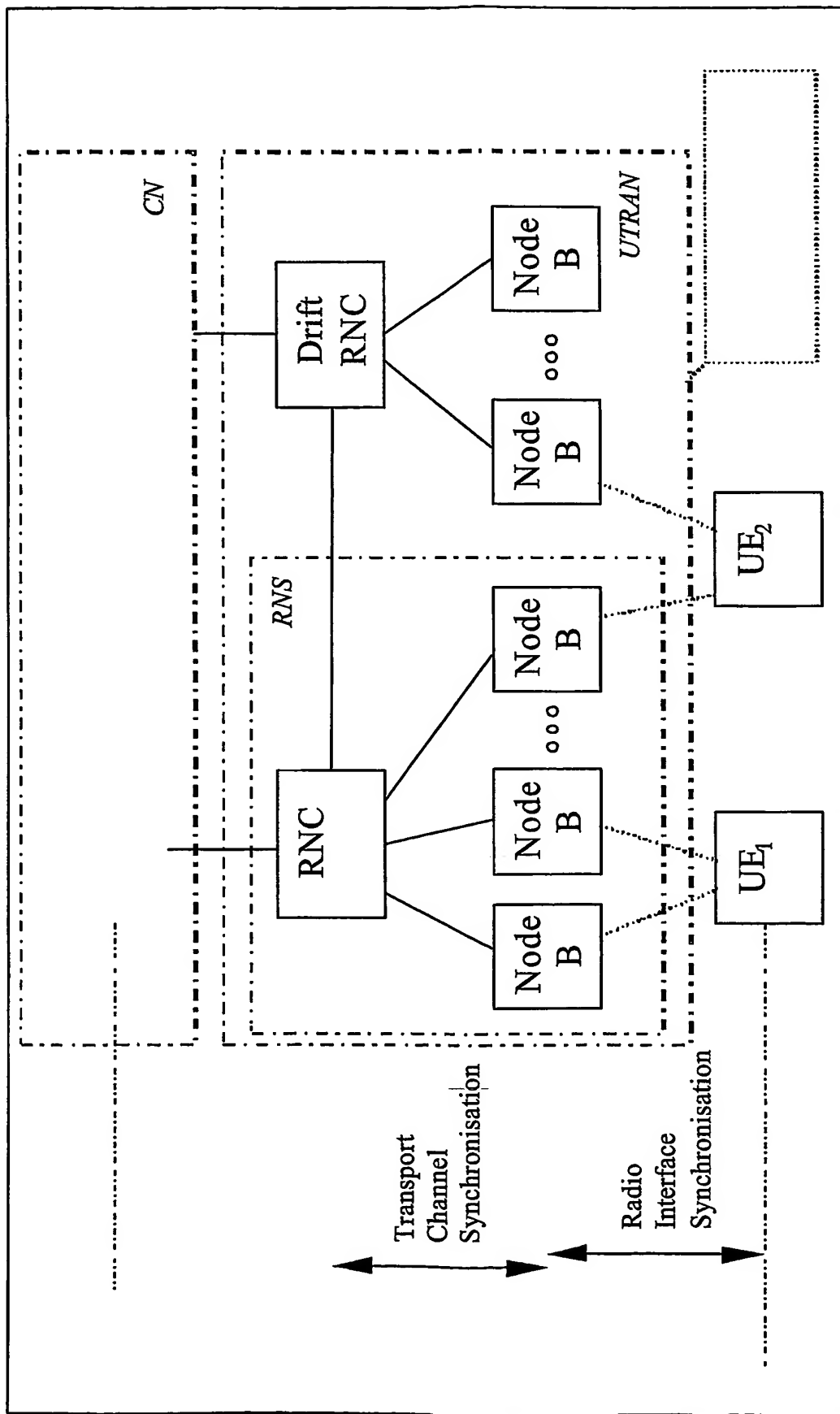


Figure 1

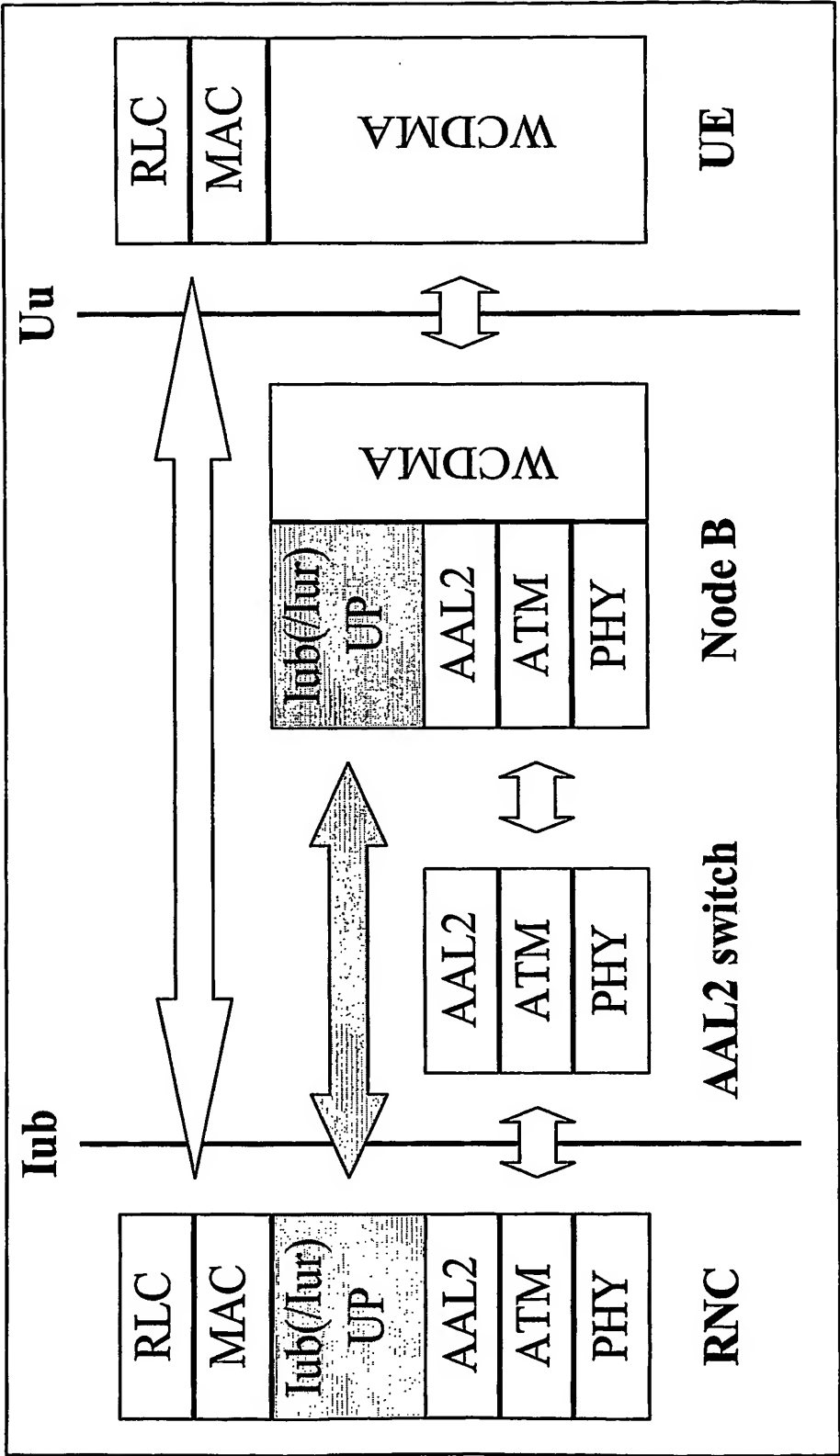
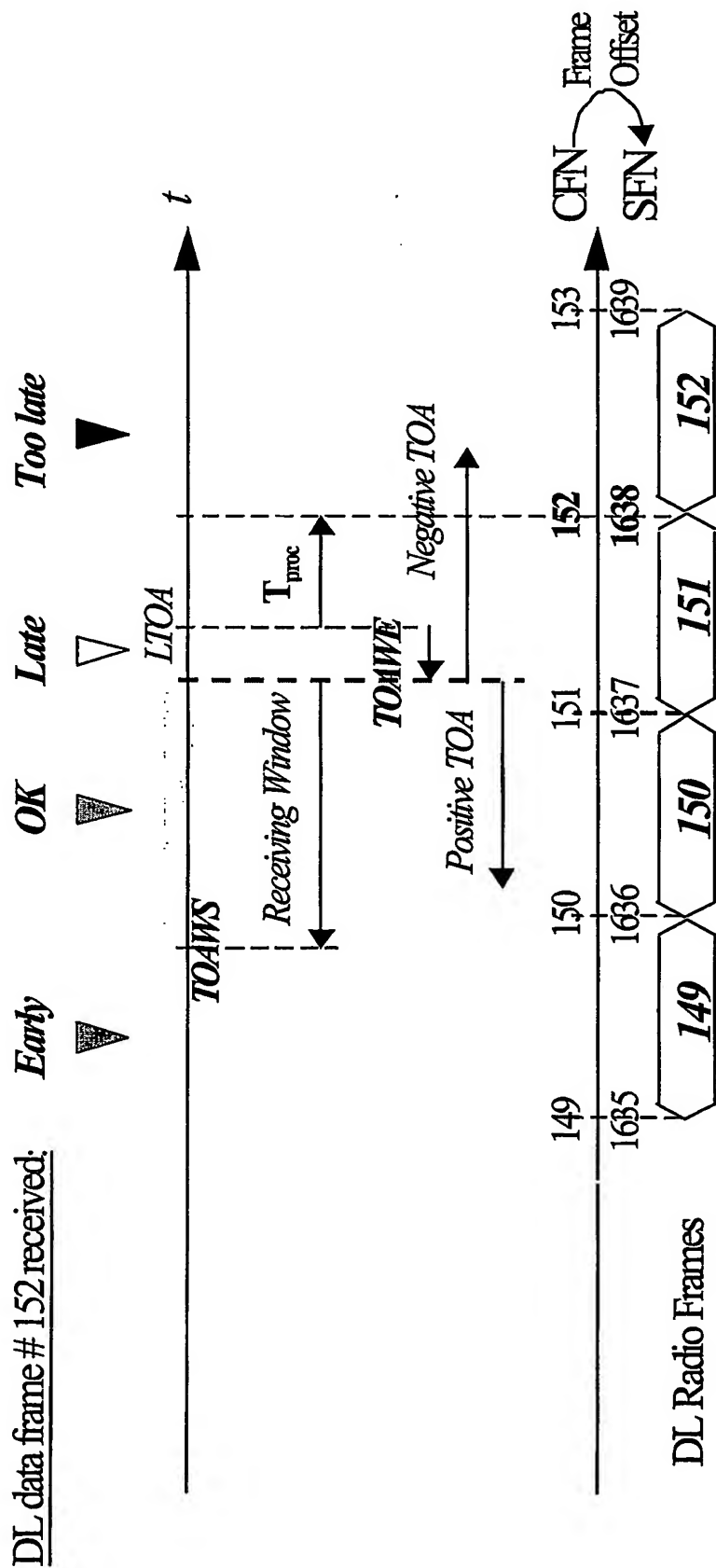


Figure 2



[FDD - Note: in this figure it is assumed that Chip Offset = 0]

TOA	Time Of Arrival	TOAWE	TOA Window Endpoint
LTOA	Latest Time Of Arrival	T_{proc}	Processing time before transmission on air-interface
TOAWS	TOA Window Startpoint		

Figure 3

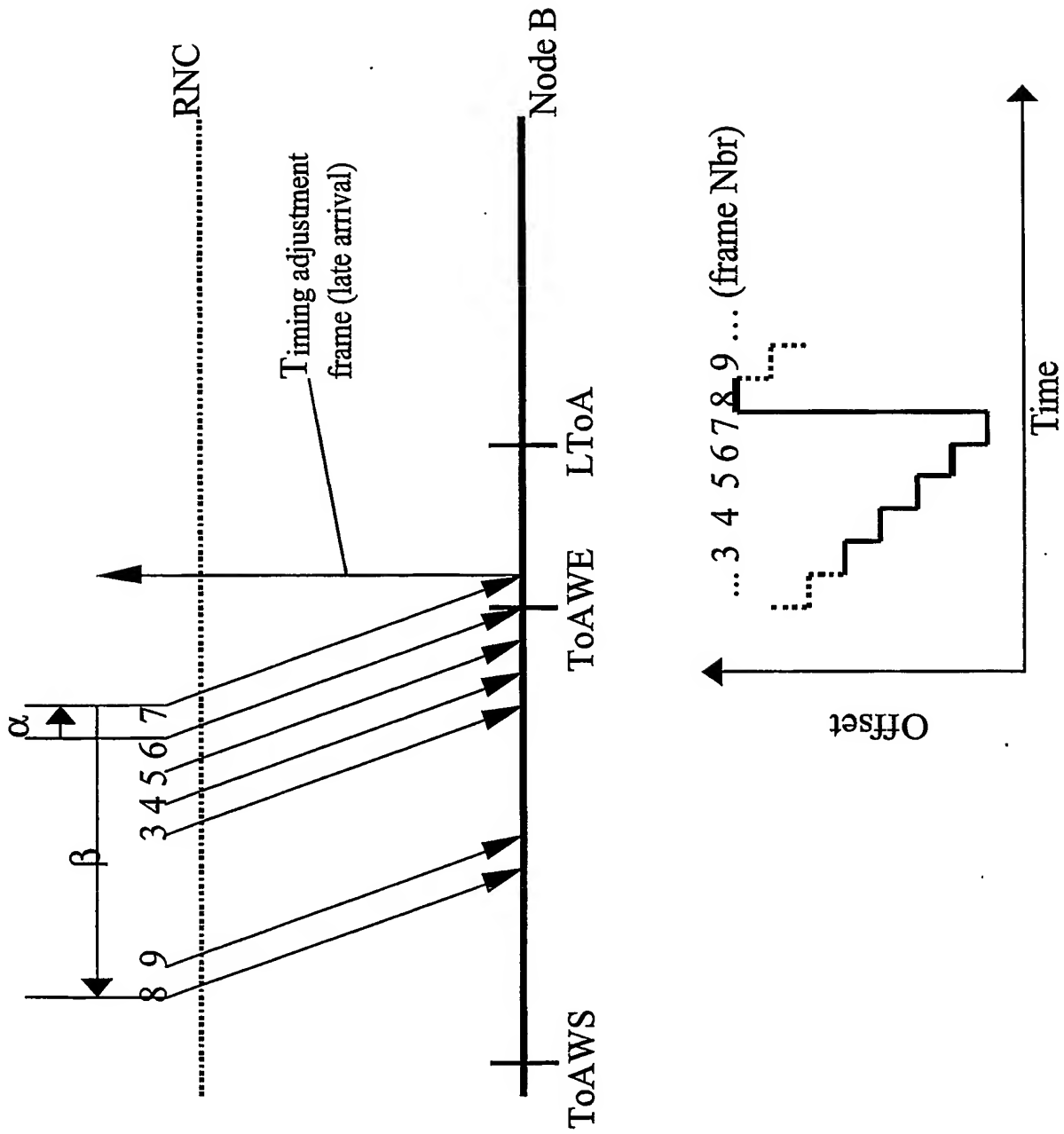


Figure 4

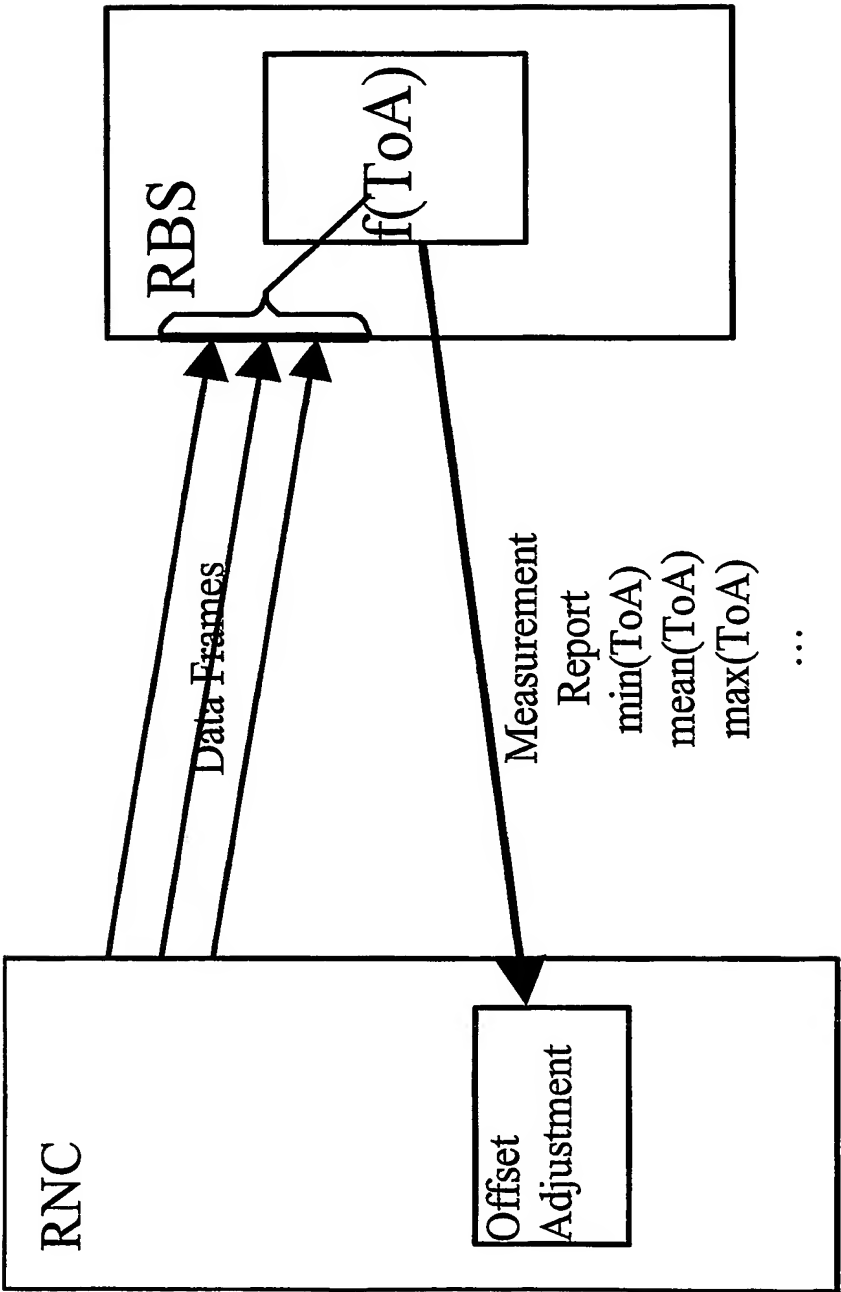


Figure 5